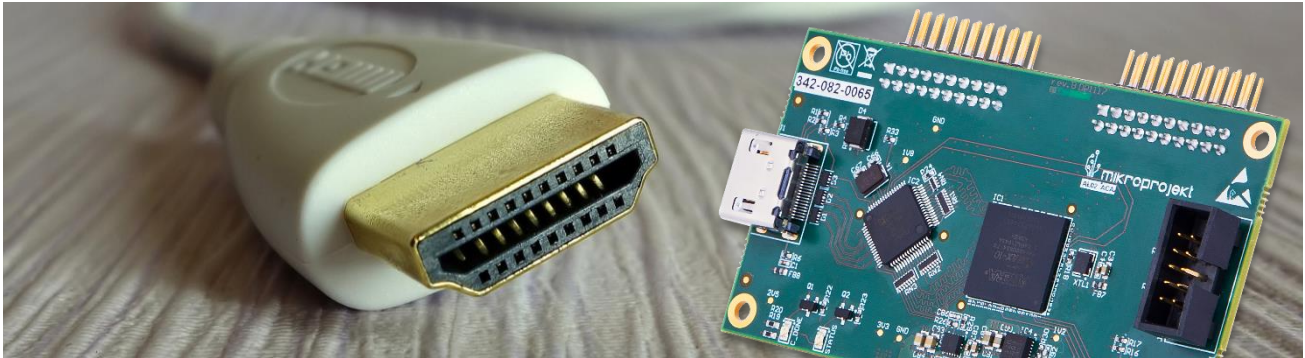


HDMI to OpenLDI converter

High definition video input adapter for the Intel 10M50 Max 10 Evaluation kit



The HDMI to OpenLDI converter allows developers to easily interface the Intel Max 10 FPGA with HDMI streaming video, supporting the conversion of video resolutions up to 1080p60 to dual link OpenLDI.

The converter is designed to plug directly into the Intel 10M50 evaluation kit, connecting to the dual LVDS differential headers capable of receiving OpenLDI. With 1080p60, the OpenLDI interface operates at 520 Mb/s per lane, and is compatible with speeds up to 640 Mb/s per lane.

The HDMI to OpenLDI converter is an easy way to attach reliable high speed video input to the 10M50 evaluation kit, already equipped with a 1080p60 capable HDMI output. With the 50K LUTs and LPDDR2 memory available, the combination makes the 10M50 evaluation kit a compelling option for the development of high speed video processing applications.

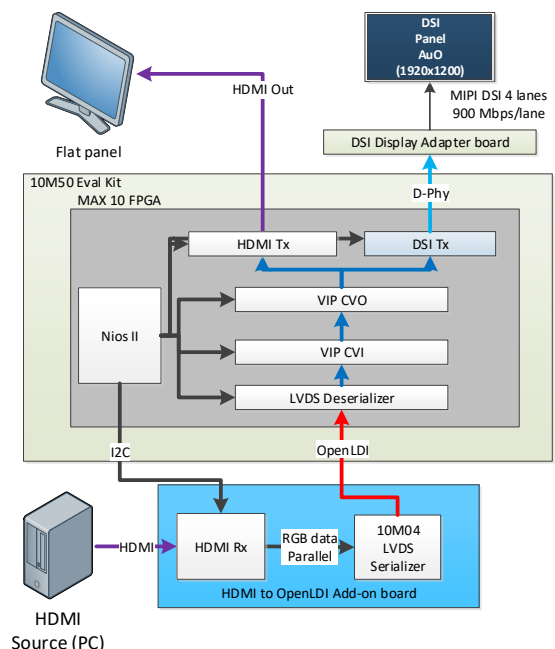
The FPGA and the ADV7611 are controlled by I2C from the Nios II CPU on the 10M50 Evaluation kit.

The onboard FPGA offers a JTAG port and can be adapted by the user for different purposes if necessary.

Feature

- HDMI 1.4b input
- Maximum resolution 1920x1080@60Hz
- ADV7611 Video Decoder
- Dual-link OpenLDI (7:1 LVDS) output
 - 640 Mbps per lane.
- FPGA: 10M04DCU324I7G
- Powered directly from the 10M50 Evaluation kit
- I2C Controlled
- Reference designs available for 10M50 (Receiver) and 10M04 (transmitter)

Application



Ordering information

The HDMI to OpenLDI Converter can be ordered at Mikroprojekt via the order code

H-10MA-HDMI

For more information, please visit www.mikroprojekt.eu